





5.00 credits

30.0 h + 30.0 h

Q1

| | |
|---------------------|---|
| Teacher(s) | Legat Jean-Didier ; |
| Language : | English > French-friendly |
| Place of the course | Louvain-la-Neuve |
| Main themes | Combinational logic circuits and sequential logic design. Digital building blocks (ALU, registers, ...). Hardware description language (SystemVerilog). Microarchitecture of a 32-bit RISC processor (single-cycle processor, multicycle processor and pipelined processor). Embedded processor architecture and I/O systems. |
| Learning outcomes | <p>At the end of this learning unit, the student is able to :</p> <p>In consideration of the reference table AA of the program "master in electrical engineering ", this course contributes to the development, to the acquisition and to the evaluation of the following experiences of learning:</p> <ul style="list-style-type: none"> • AA1.1, AA1.2 • AA2.1, AA2.2, AA2.3, AA2.4 <p>1</p> |

| Programmes containing this learning unit (UE) | | | | |
|--|------------------------|---------|--------------|---|
| Program title | Acronym | Credits | Prerequisite | Learning outcomes |
| Master [120] in Biomedical Engineering | GBIO2M | 5 | |  |
| Master [120] in Electrical Engineering | ELEC2M | 5 | |  |
| Master [120] in Computer Science and Engineering | INFO2M | 5 | |  |
| Master [120] in Electro-mechanical Engineering | ELME2M | 5 | |  |